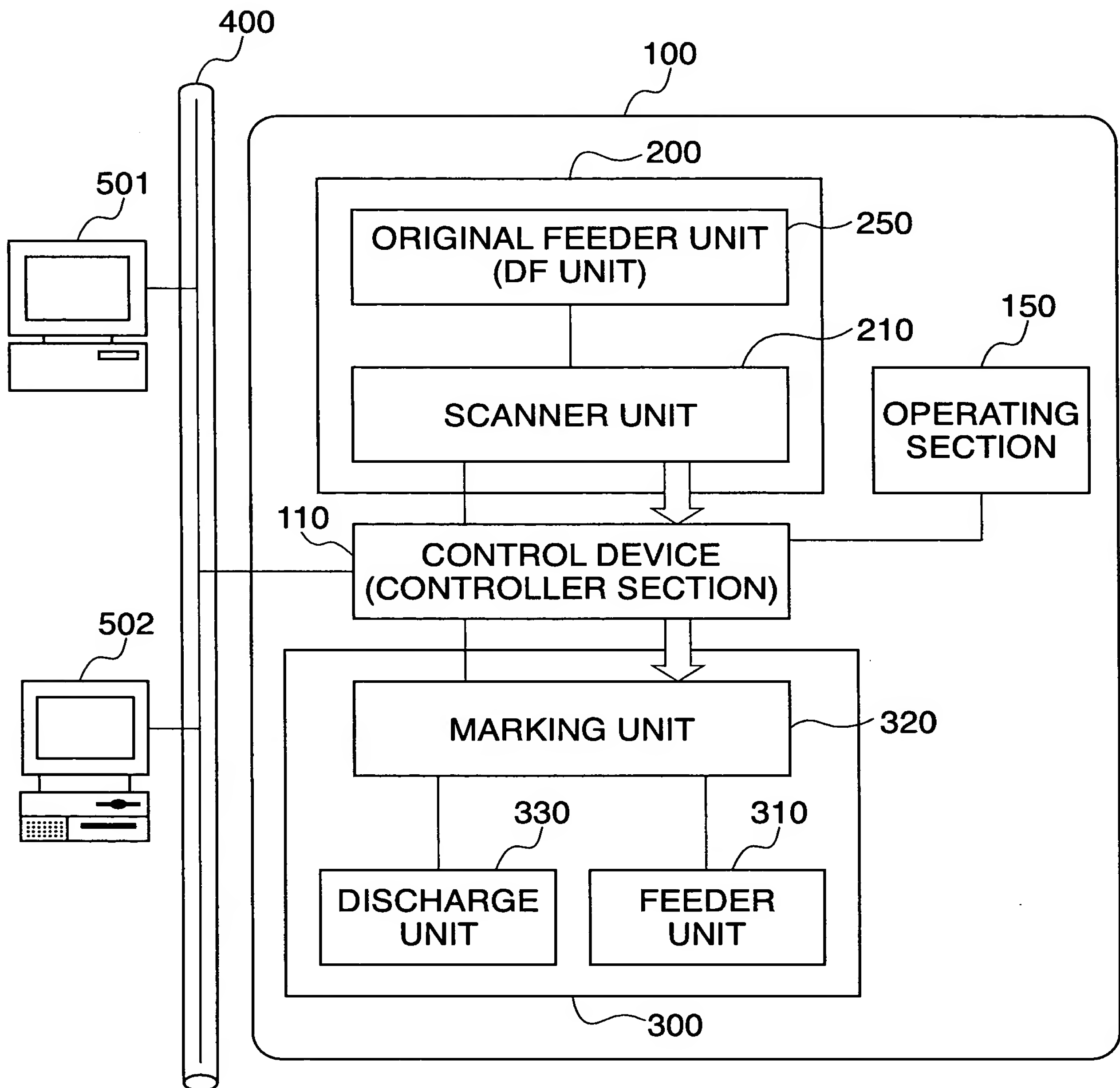


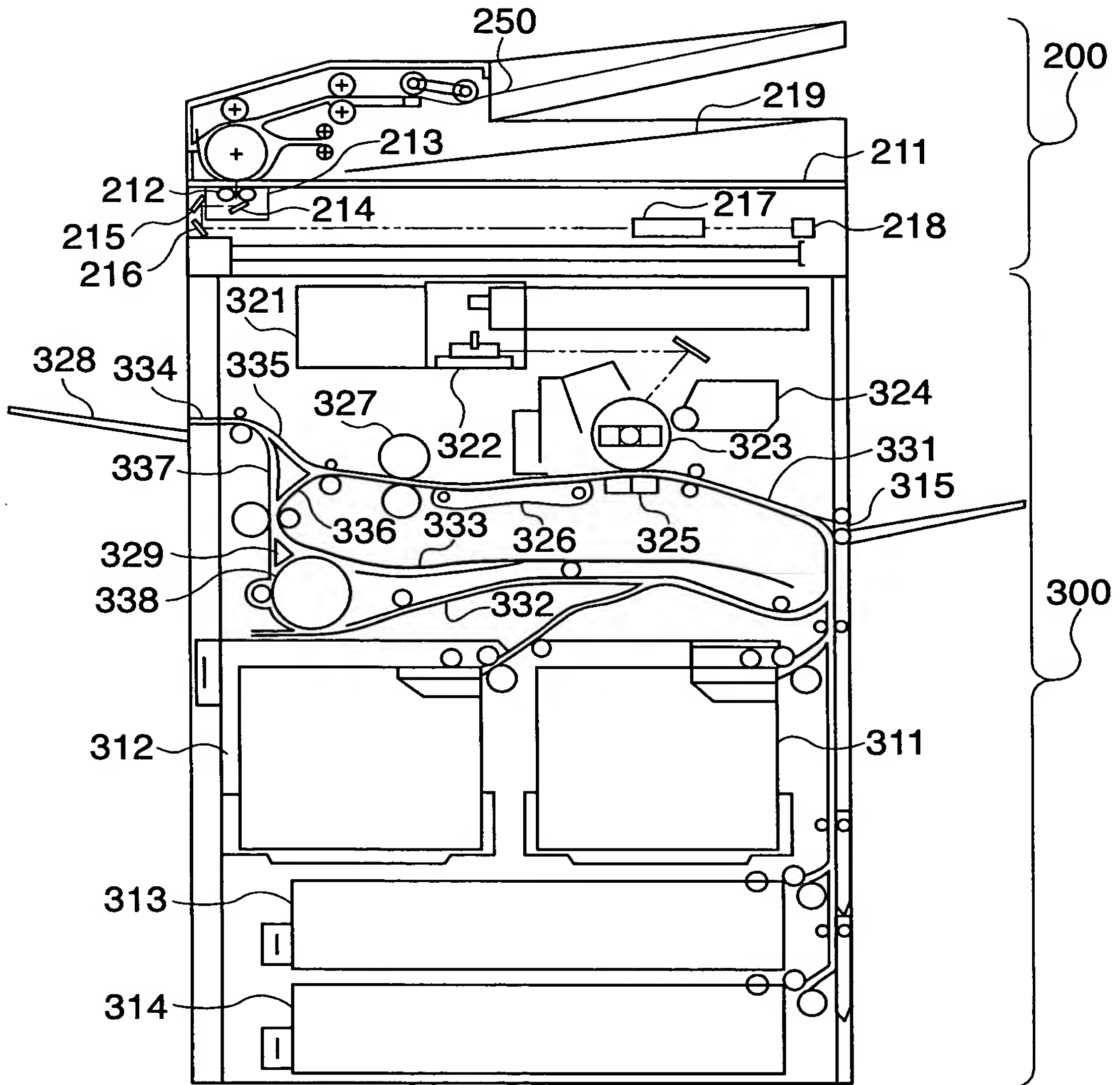
1/9

FIG. 1



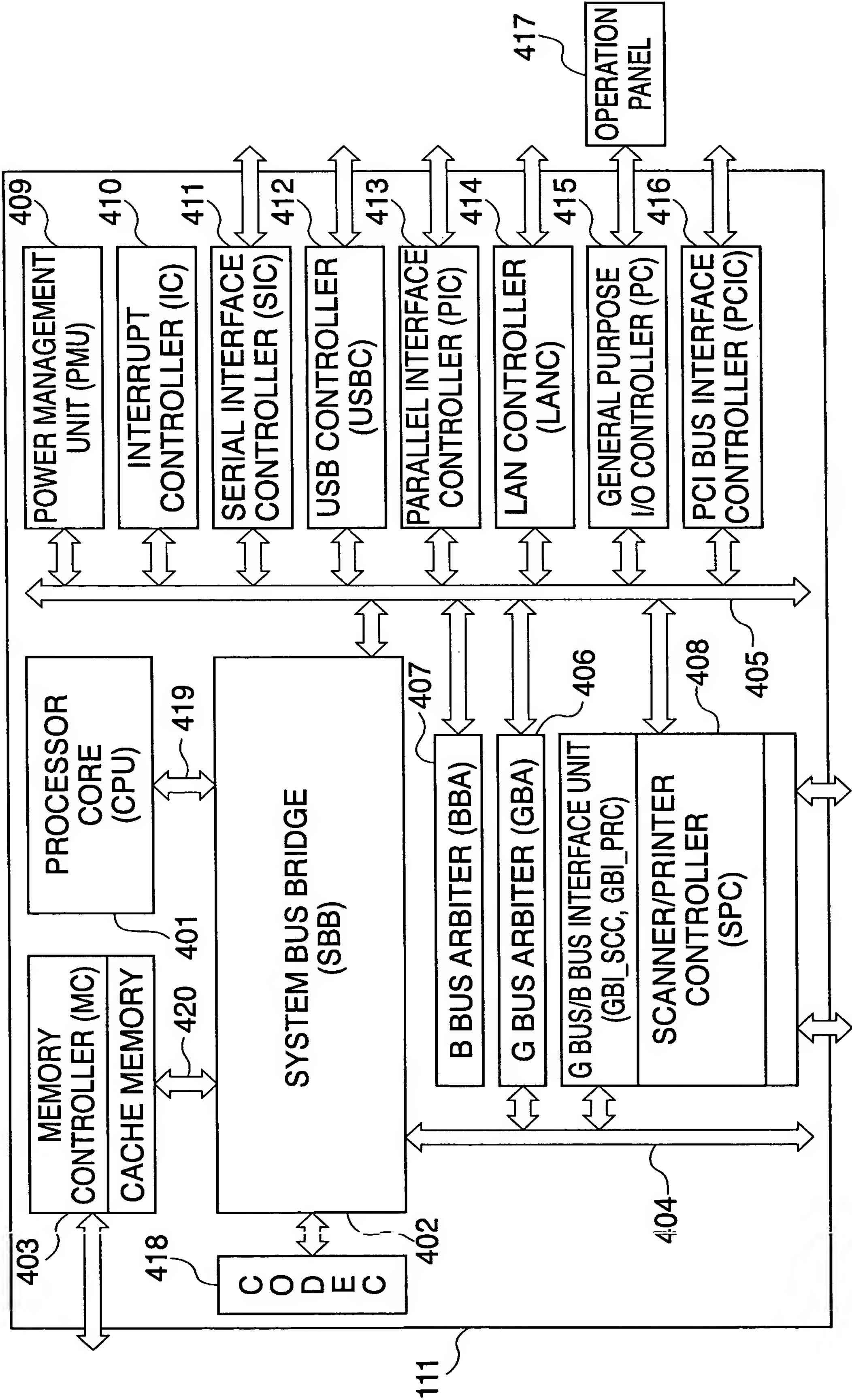
2/9

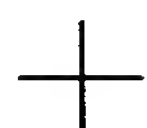
FIG. 2



The diagram illustrates a computer system architecture with a central **MAIN CONTROLLER** (111) at its core. The main controller contains a **CPU** (112) and a **BUS CONTROLLER** (113). It is connected to **ROM** (114) and **DRAM** (116) via a bus (117). A **NETWORK CONTROLLER** (121) is connected to the main controller via a bus (123) and to an external **CONNECTION** (122). The main controller is also connected to an **I/O CONTROL SECTION** (126), which includes a **SERIAL COMMUNICATION CONTROLLER (2ch)** (127). This section is connected to an **E-IDE I/F** (161) and a **REAL-TIME CLOCK MODULE** (130). The real-time clock module is connected to a **BACKUP BATTERY** (134). The main controller is also connected to a **CONNECTION** (124) and an **EXPANSION CONNECTOR** (125). The main controller is connected to a **SCANNER INTERFACE** (143) and a **PRINTER INTERFACE** (145). The scanner interface is connected to a **CONNECTION** (142) and a **SCANNER** (144). The printer interface is connected to a **CONNECTION** (148) and a **PRINTER** (149). The main controller is also connected to a **LCD CONTROL-ER** (131) and a **PANEL INTERFACE** (132). The LCD control-er is connected to a **CONNECTION** (131a) and a **PANEL** (133).

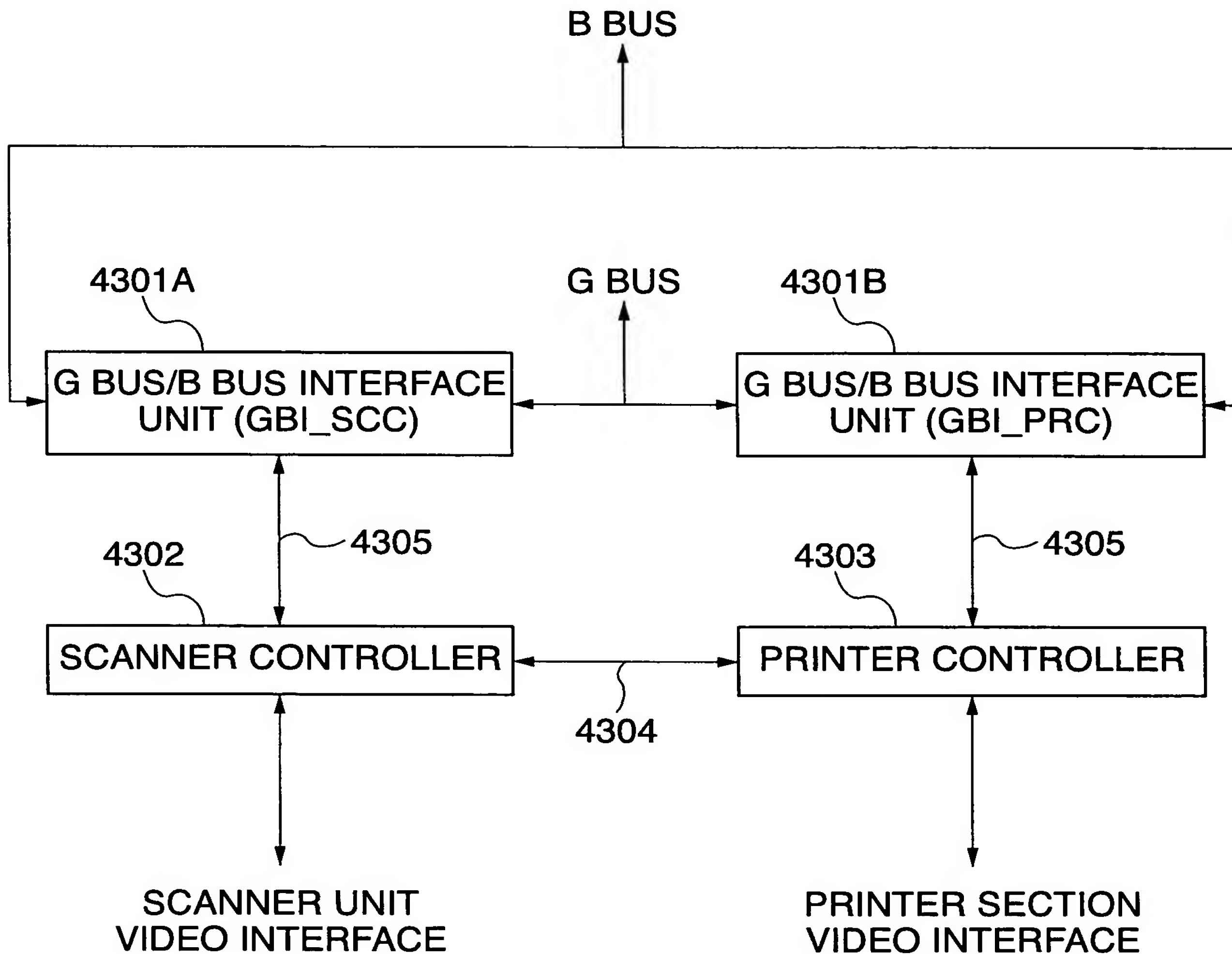
FIG. 4





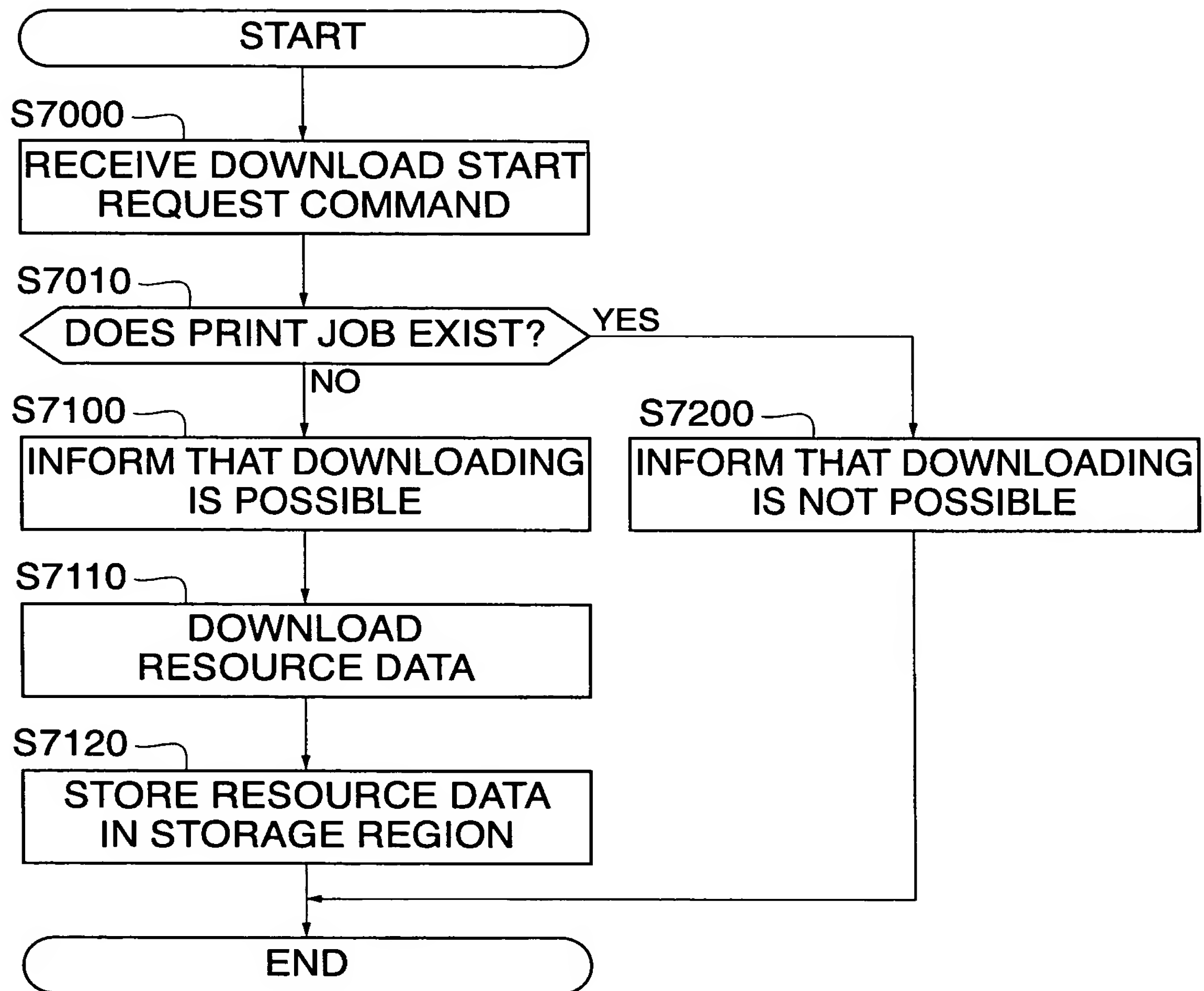
6/9

FIG. 6



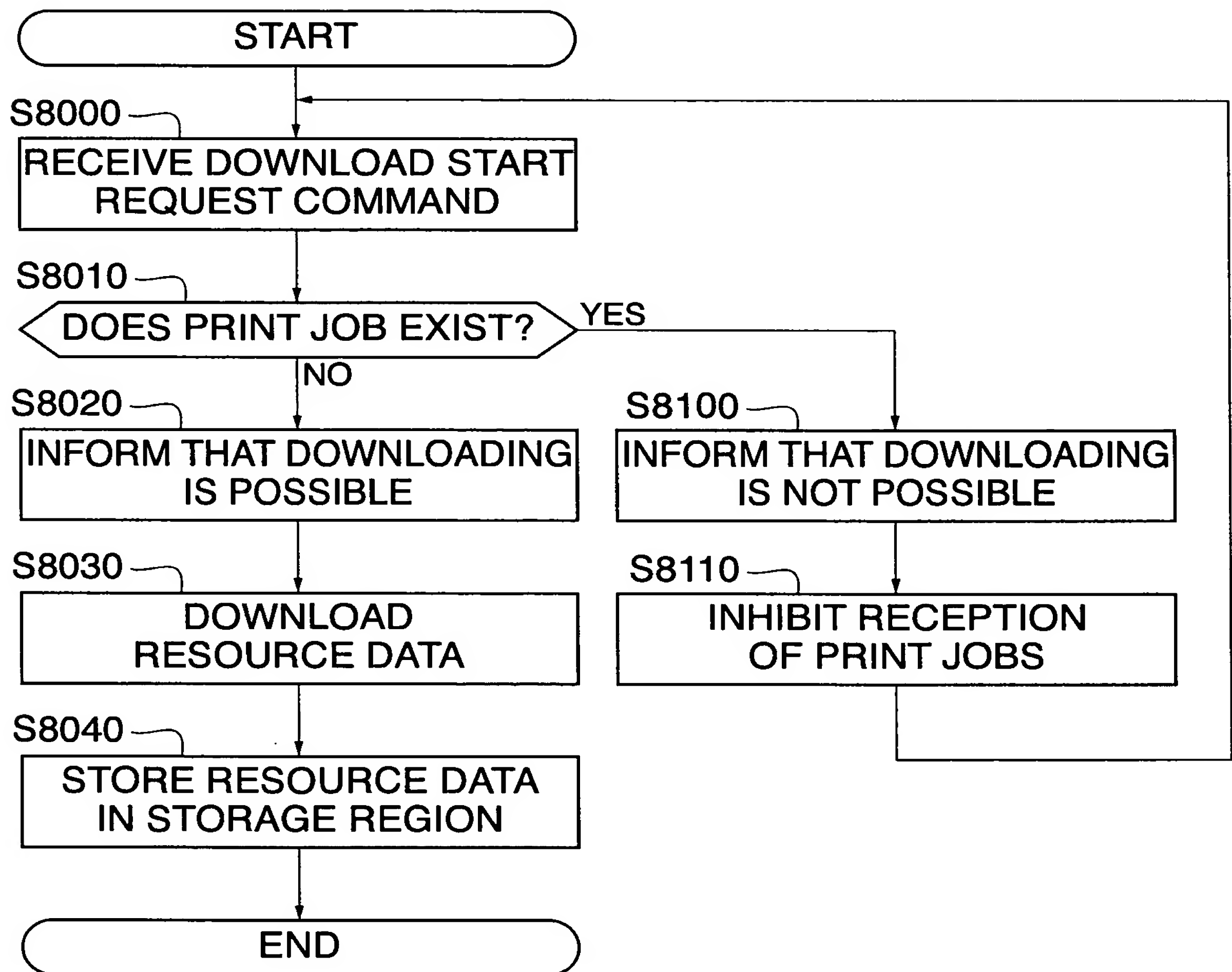
7/9

FIG. 7



8/9

FIG. 8



9/9

FIG. 9

